

Fig. 1 is a block diagram of a system architecture.

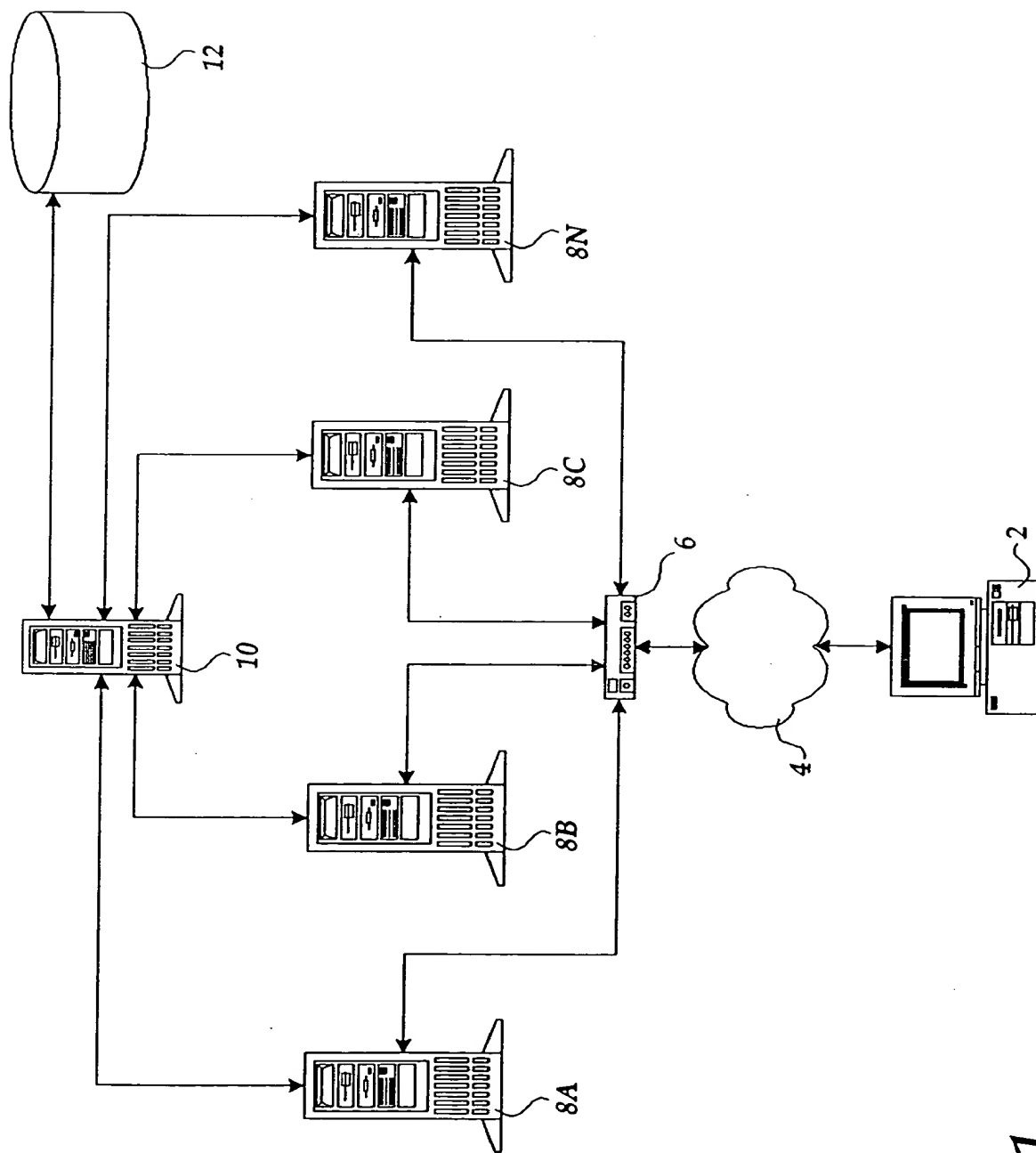


Fig.1

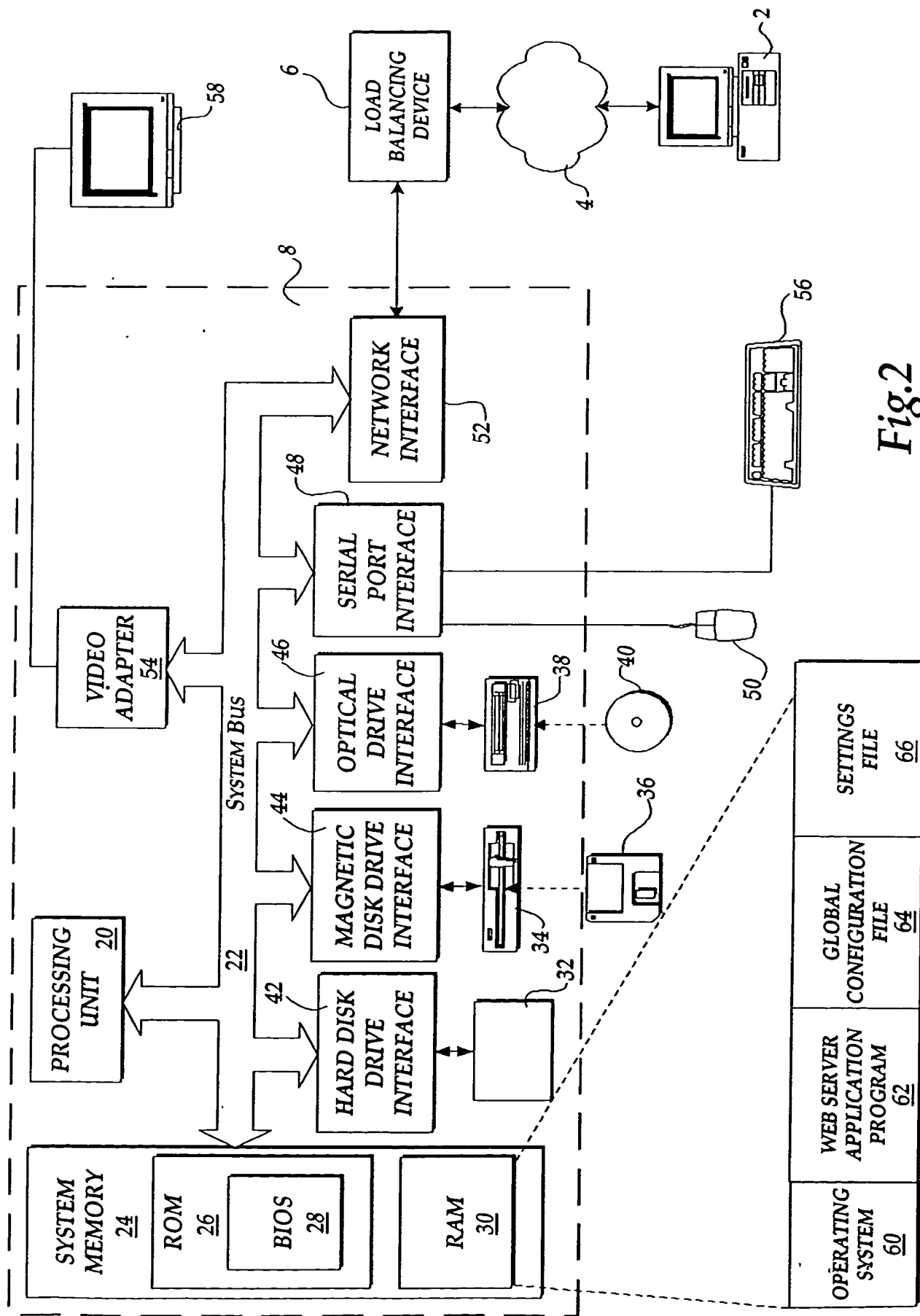


Fig.2

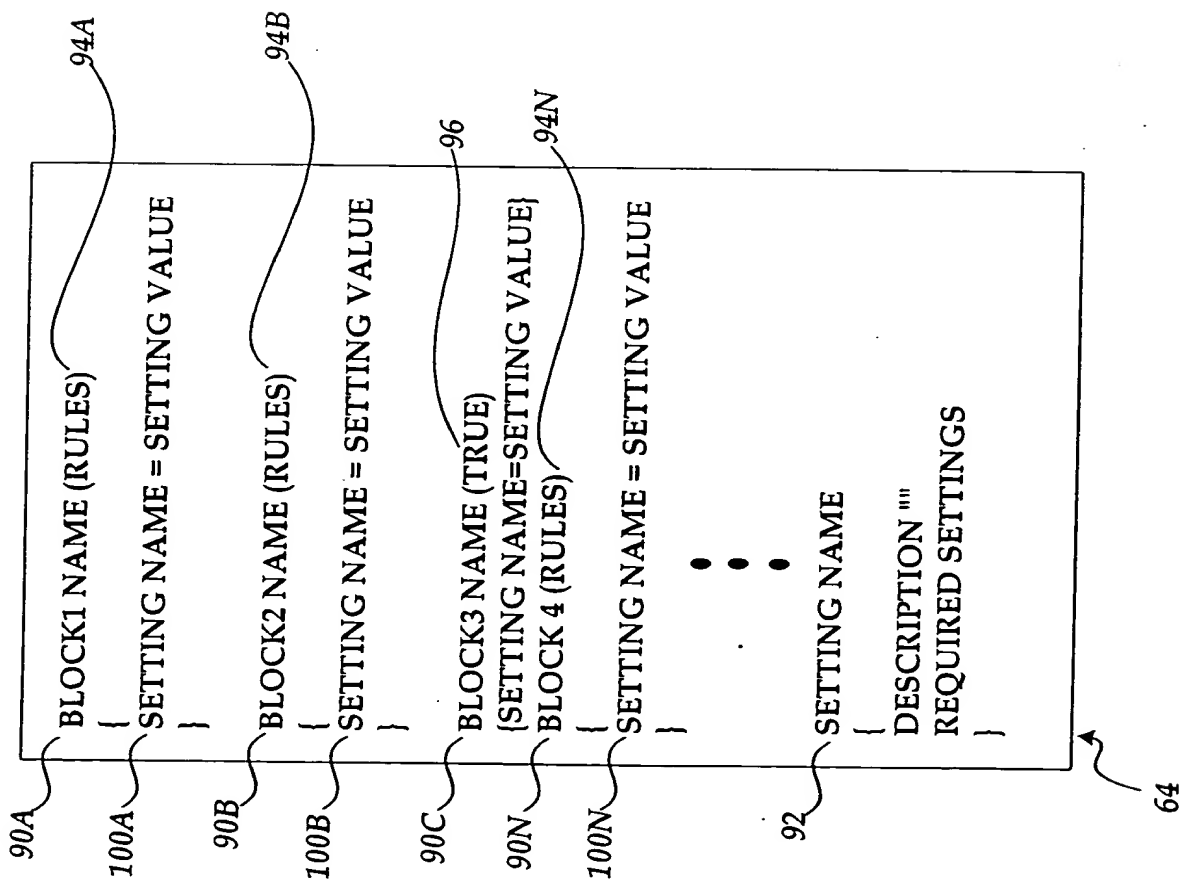


Fig.3A

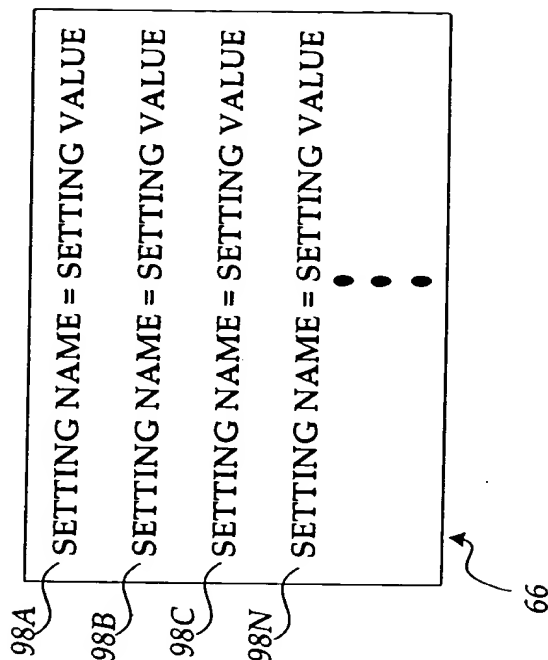


Fig.3B

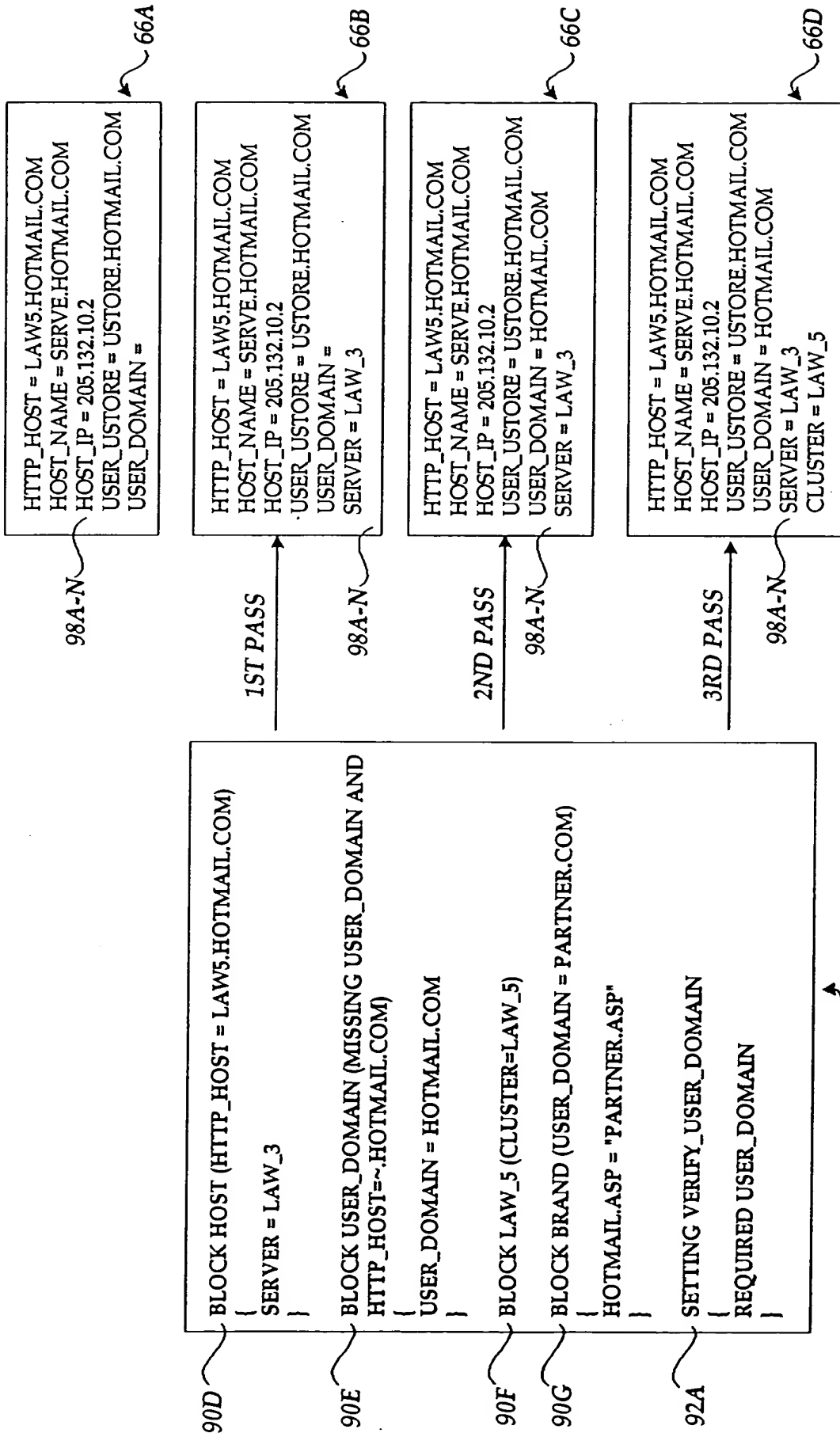
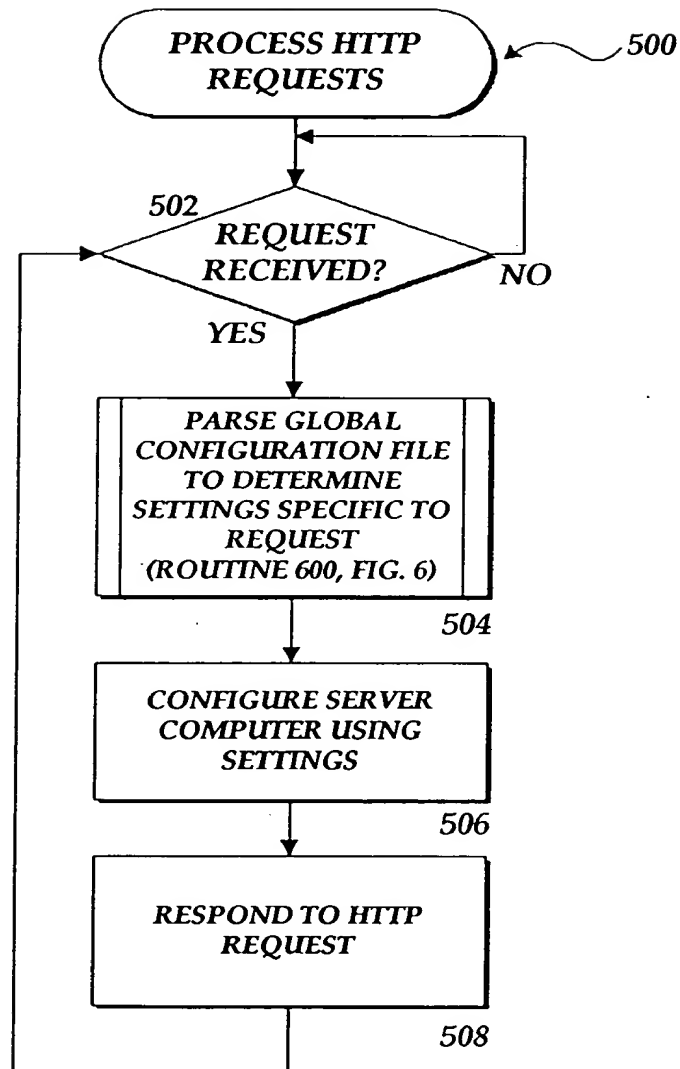


Fig.4



*Fig.5*

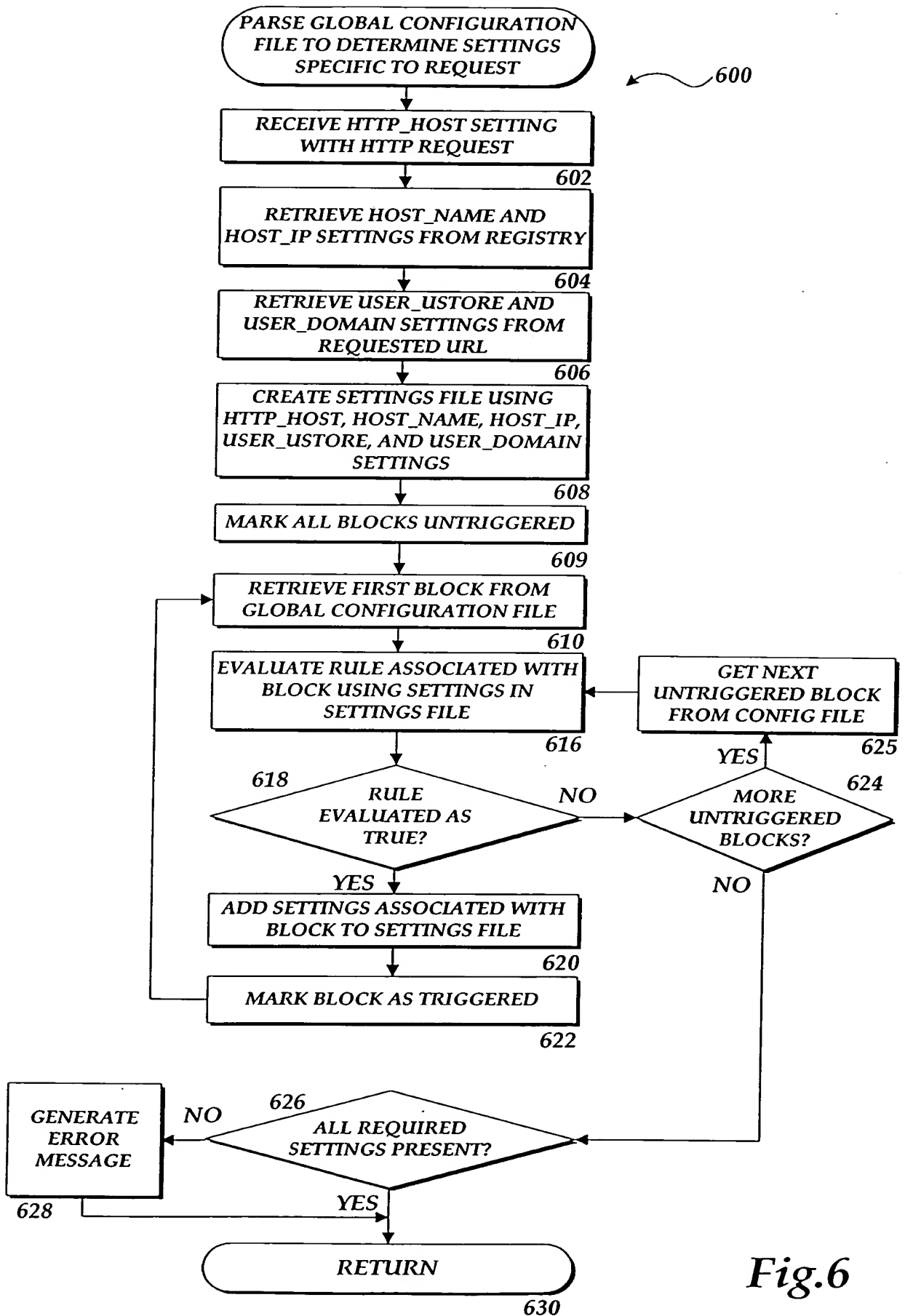


Fig.6